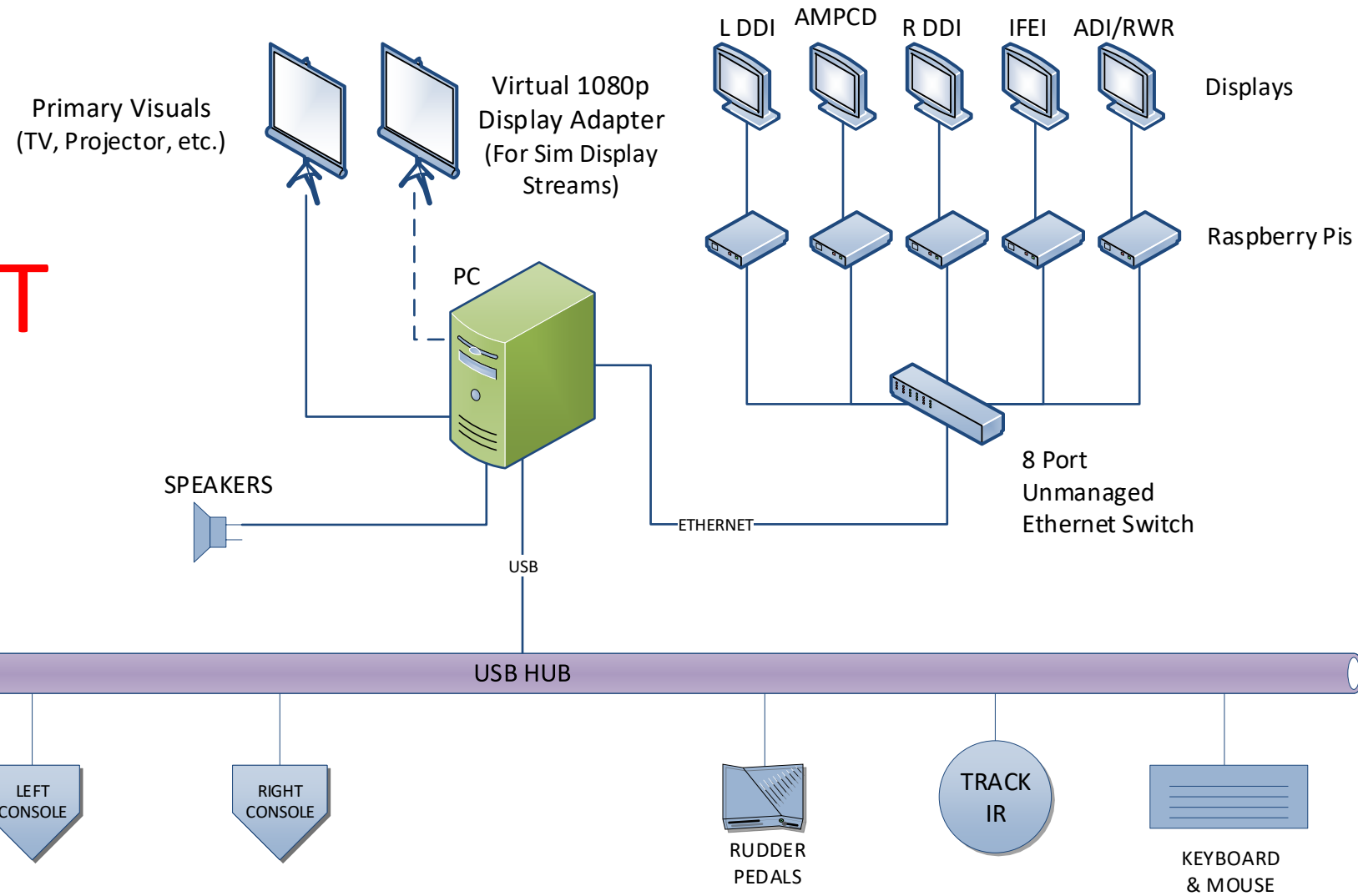


OpenHornet System Architecture

October 13, 2018

DRAFT

TOP LEVEL ARCHITECTURE



ARDUINO BASED SIMULATOR INTERFACE SYSTEM (ABSIS)

BASIC I/O CAPABILITIES

ABSIS NANO

- 8x ANALOG I/O
- 3X DIGITAL I/O (PWM – 5V)
- 3X DIGITAL I/O (PWM – 12V/5V SELECTABLE)
- 5X DIGITAL I/O (NO PWM)

ABSIS MEGA

- 16x ANALOG I/O
- 8X DIGITAL I/O (PWM – 5V)
- 3X DIGITAL I/O (PWM – 12V/5V SELECTABLE)
- 31X DIGITAL I/O (NO PWM)

